



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/773,410	02/09/2004	Takashi Miyazawa	118374	6611
25944	7590	07/24/2007	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			SHANKAR, VIJAY	
		ART UNIT	PAPER NUMBER	
		2629		
		MAIL DATE	DELIVERY MODE	
		07/24/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/773,410	MIYAZAWA, TAKASHI	
	<b>Examiner</b>	<b>Art Unit</b>	
	VIJAY SHANKAR	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 02 May 2007.  
 2a) This action is FINAL.                  2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-19 is/are pending in the application.  
 4a) Of the above claim(s) 1-13 is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 14-19 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____                                      |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

**DETAILED ACTION**

***Priority***

1. Receipt is acknowledged of a certified copy of the 2/9/2004 application referred to in the oath or declaration or in an application data sheet. If this copy is being filed to obtain the benefits of the foreign filing date under 35 U.S.C. 119(a)-(d), applicant should also file a claim for such priority as required by 35 U.S.C. 119(b). If the application being examined is an original application filed under 35 U.S.C. 111(a) (other than a design application) on or after November 29, 2000, the claim for priority must be presented during the pendency of the application, and within the later of four months from the actual filing date of the application or sixteen months from the filing date of the prior foreign application. See 37 CFR 1.55(a)(1)(i). If the application being examined has entered the national stage from an international application filed on or after November 29, 2000, after compliance with 35 U.S.C. 371, the claim for priority must be made during the pendency of the application and within the time limit set forth in the PCT and Regulations of the PCT. See 37 CFR 1.55(a)(1)(ii). Any claim for priority under 35 U.S.C. 119(a)-(d) or (f) or 365(a) or (b) not presented within the time period set forth in 37 CFR 1.55(a)(1) is considered to have been waived. If a claim for foreign priority is presented after the time period set forth in 37 CFR 1.55(a)(1), the claim may be accepted if the claim properly identifies the prior foreign application and is accompanied by a grantable petition to accept an unintentionally delayed claim for priority. See 37 CFR 1.55(c).

***Claim Objections***

2. Claim 15 is objected to because of the following informalities: Claim 15, line 1, delete second "first" before "voltage". Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 14-19 are rejected under 35 U.S.C. 102(e) as being anticipated by Kimura (US 6,781,567).

Regarding Claims 14, 18, Kimura teaches a method of driving an electro-optical device having scanning lines, data lines, and pixel circuits each of which includes an electro-optical element and a driving transistor, (Figures 1-4) the method comprising: setting a potential of a controlling terminal of the driving transistor to a first voltage level, the setting of the potential of the controlling terminal to the first voltage level (Col.3, lines 34-61) including electrically connecting one of a source and a drain of the driving transistor to the controlling terminal during a first period (Figures 1-4; Column 6, line 53- Column 8, line 21); setting the potential of the controlling terminal to

Art Unit: 2629

a second voltage level by using a capacitive coupling occurring at a capacitive element connected to the controlling terminal, the setting of the potential of the controlling terminal to the second voltage level (Col.3, lines 34-61) including applying a data voltage to the capacitor through one data line of the data lines and a switching transistor (Figures 1-4; Column 6, line 53- Column 8, line 21); and supplying a driving current or a driving voltage to the electro-optical element, the driving current or the driving voltage having a level corresponding to a conduction state of the driving transistor. (Figures 1-4; Summary; Column 6, line 53- Column 8, line 21).

Regarding Claim 15, Kimura teaches the method the first voltage level being a voltage level the driving transistor in an off-state. (Col.3, lines 34-61).

Regarding Claims 16, 19, Kimura teaches the method the pixel circuits including a plurality of group of pixel circuits each of which is disposed along one scanning line of the scanning lines, and each of the plurality of groups of pixel circuits including electro-optical elements for an identical color, and each of the plurality of groups of pixel circuits including electro-optical elements that emit an identical color. (Figures 1-4; Summary; Column 6, line 53- Column 8, line 21).

Regarding Claim 17, Kimura teaches the method according to claim 14, the electro-optical element being EL element (Fig.4; Col.8, line 4-21).

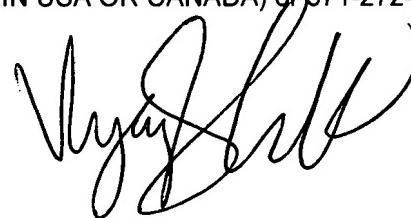
5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Knapp et al, Koyama both teach EL display.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR  
Primary Examiner  
Art Unit 2629

VS